**Weekly Statue Report**

**WCP52**

We team members had a meeting this Monday at senior design lab. During this meeting, we improved our Project Requirements Specification. We added some requirements and struck several requirements from the suggestions that Kyle give us last week. And our leader also made the KiCad let us to download.

We have a meet today with Professor Kyle, and show him our Project Requirements Specification. And here are something needs us to notice and change.

1. We need to change something about the standard Bode Plot spec.

- describe what is meant by standard bode plot or get rid of the word standard.

- or a plot of gain with respect to frequency.

2. SHALL go to 150MHz, and SHOULD go to 200MHz and keep amplitude spec up to 100 MHz

3. 3.2.5 change 80 to 60, and SHALL do 30dB instead of 40dB and should do 60dB.

4. should have a more precise phase accuracy(3 degrees)

Here is something we need to think about that might kill our project at the last, and know how we will avoid these problems.

1. supply chain issues
2. budget issues
3. run out of time for software/testing
4. general test-ability issues
5. prototype breaking

From next week our next step is to figure out overall system architecture. We need to go high level than the system block diagram on the project write up. It needs to contain functional blocks and do not forget about power distribution.- figure out what is the best way to calculate phase.-figure out what might be the most difficult pieces and begin designing in such a way that make those difficult pieces easiest.